

# HA13127, HA13130

## 17 W Dual BTL Audio Power Amplifier

The HA13127/HA13130 are high output and low distortion dual BTL power IC designed for car stereo amplifiers.

At 14.4 V to 4 Ω load, this power IC provides an output power 17 W with 10 % distortion.

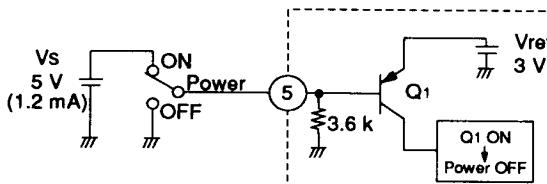
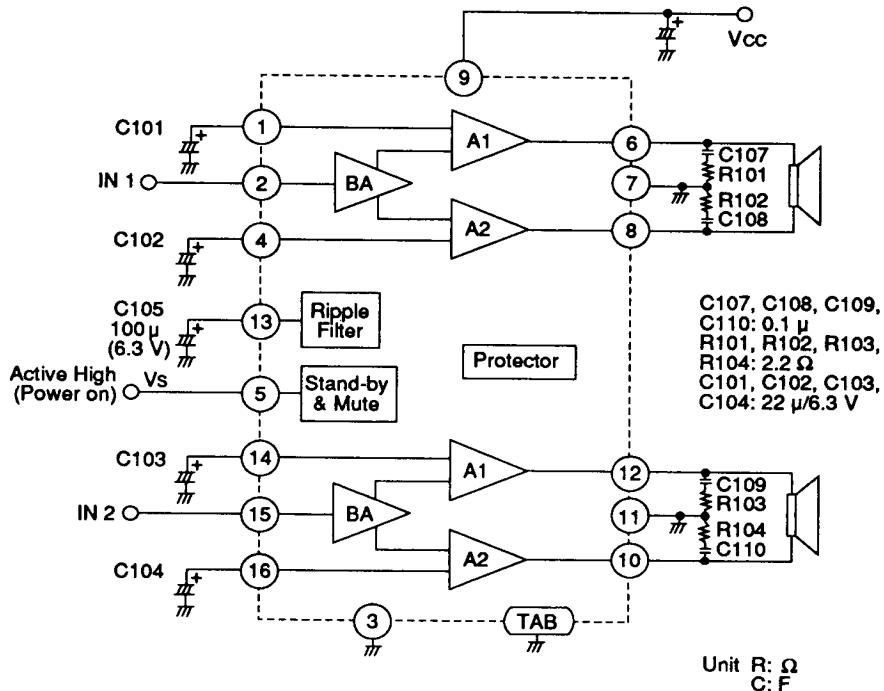
### Features

- Stand-by circuit included.  
Can be switched on & off easily by microcomputer.
- Output capacitors not required.  
These IC employ internal ASO protection circuit of high reliability current shutdown type, which can protect speaker.
- Surge protection circuit and thermal shutdown circuit are included.  
Thermal shutdown is high speed and hysteresis on & off type.
- Can be used without bootstrap capacitor.
- Low total harmonic distortion in wide frequency range  
THD = 0.05 % Typ (f = 50 Hz)  
THD = 0.05 % Typ (f = 1 kHz)  
THD = 0.07 % Typ (f = 10 kHz)  
THD = 0.1 % or less  
(Pout = 1.5 W, f = 20 Hz to 20 kHz)

### Ordering Information

Type No.	Voltage gain	Package
HA13127	50 dB	16 pin SIP
HA13130	40 dB	with heat sink





## 2. Capacitor

C107, C108, C109, C110 must be non secondary resonance type (non inductive type) polyester film capacitor for keeping stability.

**Figure 1 Block Diagram**



## HA13127, HA13130

### Absolute Maximum Ratings ( $T_a = 25^\circ\text{C}$ )

Item	Symbol	Rating	Unit	Notes
Operating supply voltage	V <sub>CC</sub>	18	V	
DC supply voltage	V <sub>CC</sub> (DC)	26	V	1
Peak supply voltage	V <sub>CC</sub> (Peak)	50	V	2
Output current	I <sub>O</sub> (peak)	4	A	Per channel
Power dissipation	P <sub>T</sub>	25	W	
Junction temperature	T <sub>J</sub>	150	°C	
Operating temperature	T <sub>OPR</sub>	-30 to +85	°C	
Storage temperature	T <sub>STG</sub>	-55 to +125	°C	

Notes: 1. Value at  $t \leq 30$  sec

2. Value at surge wave-form (rise time  $t \geq 1$  ms)

### Electrical Characteristics ( $V_{CC} = 13.2$ V, $f = 1$ kHz, $R_L = 4$ Ω, dual operation, $T_a = 25^\circ\text{C}$ )

HA13127 (G<sub>V</sub> = 50 dB) HA13130 (G<sub>V</sub> = 40 dB)

Item	Symbol	Mln	Typ	Max	Mln	Typ	Max	Unit	Test Conditions
Quiescent current	I <sub>Q1</sub>	60	150	250	60	150	250	mA	V <sub>in</sub> = 0 V
Input bias voltage	V <sub>B</sub>	—	20	40	—	20	40	mV	V <sub>in</sub> = 0 V
Output offset voltage	ΔV <sub>O</sub>	—	0	150	—	0	150	mV	V <sub>in</sub> = 0 V
Voltage gain	G <sub>V</sub>	48.5	50	51.5	38.5	40	41.5	dB	
Difference of voltage gain	ΔG <sub>V</sub>	—	—	1.5	—	—	1.5	dB	
Output power (1)	P <sub>O1</sub>	10	14	—	10	14	—	W	V <sub>CC</sub> = 13.2 V THD = 10 %
Output power (2)	P <sub>O2</sub>	—	17	—	—	17	—	W	V <sub>CC</sub> = 14.4 V THD = 10 %
Output power (3)	P <sub>O3</sub>	—	6	—	—	11	—	W	V <sub>CC</sub> = 13.2 V THD = 1 %
Total harmonic distortion	THD	—	0.2	0.7	—	0.04	0.15	%	P <sub>out</sub> = 1.5 W



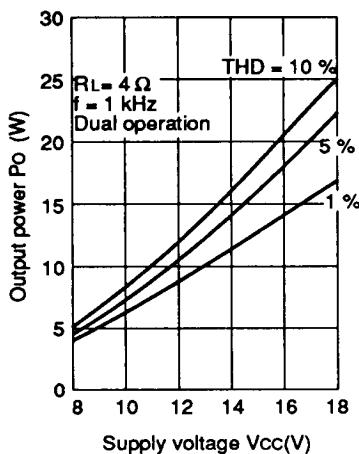
**HA13127, HA13130**

**Electrical Characteristics (V<sub>CC</sub> = 13.2 V, f = 1 kHz, R<sub>L</sub> = 4 Ω, dual operation, Ta = 25 °C) (cont)**

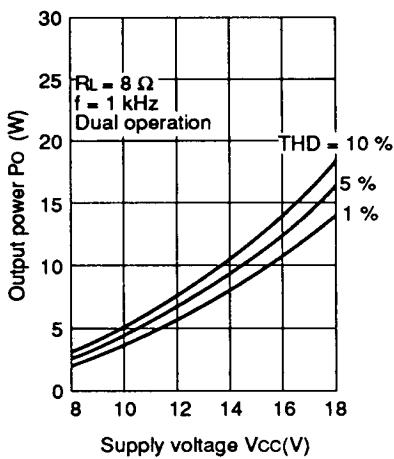
Noise output (1)	WBN <sub>1</sub>	—	1.0	2.0	—	0.35	0.7	mV	R <sub>g</sub> = 10 kΩ BW = 20 Hz to 20 kHz
Noise output (2)	WBN <sub>2</sub>	—	0.8	1.7	—	0.25	0.5	mV	R <sub>g</sub> = 0 BW = 20 Hz to 20 kHz
Supply voltage rejection ratio	SVR	32	40	—	45	60	—	dB	f = 500 Hz, Vripple = 0 dBm
Low roll-off Frequency	f <sub>L</sub> α	—	20	—	—	10	—	Hz	Δ G <sub>V</sub> = -3 dB from
High roll-off frequency	f <sub>H</sub> α	—	20	—	30	70	140	kHz	f = 1 kHz
Stand-by current	I <sub>S</sub>	—	50	200	—	50	200	μA	V 5 Open
Stand-by threshold voltage	V <sub>TH</sub> (H)	5	—	V <sub>CC</sub> -1	5	—	V <sub>CC</sub> -1	V	Vin=50 dBm Output on
Stand-by (Mute) signal reduction level	V <sub>TH</sub> (L)	0	—	1	0	—	1	V	Output off
Stand-by (Mute) on time	t <sub>F</sub>	—	10	—	—	10	—	μs	V1 = 3 V to Open (Power on to off)
Stand-by (Mute) off time	t <sub>R</sub>	—	0.2	—	—	0.2	—	sec	V1 = Open to 3 V (Power off to on)
Input impedance	R <sub>IN</sub>	20	30	40	20	30	40	kΩ	
Channel cross-talk	C <sub>T</sub>	—	60	—	45	60	—	dB	V <sub>out</sub> = 0 dBm
Output power (4)	P <sub>O4</sub>	—	10	—	—	10	—	W	THD = 10 % R <sub>L</sub> = 8 Ω
Output power (5)	P <sub>O5</sub>	—	7	—	—	7	—	W	THD = 1 % R <sub>L</sub> = 8 Ω



**Output power vs. supply voltage (1)**

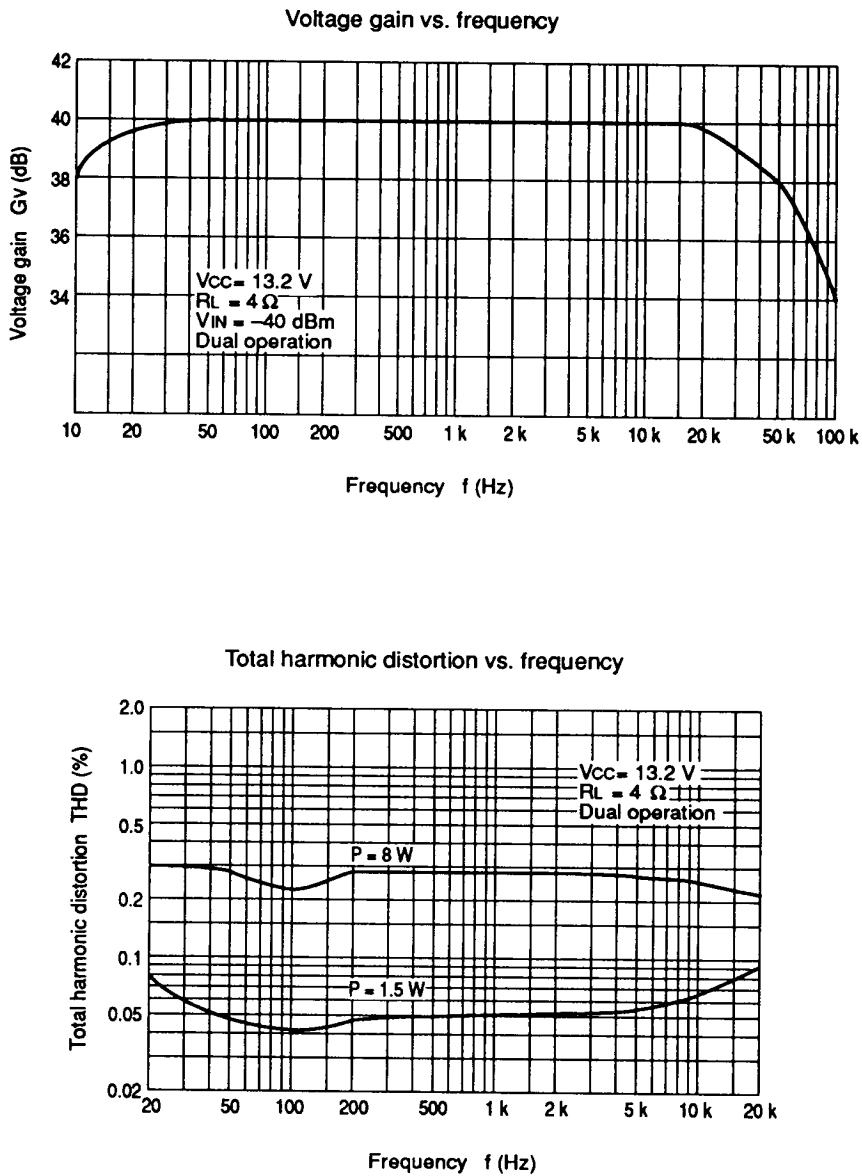


**Output power vs. supply voltage (2)**



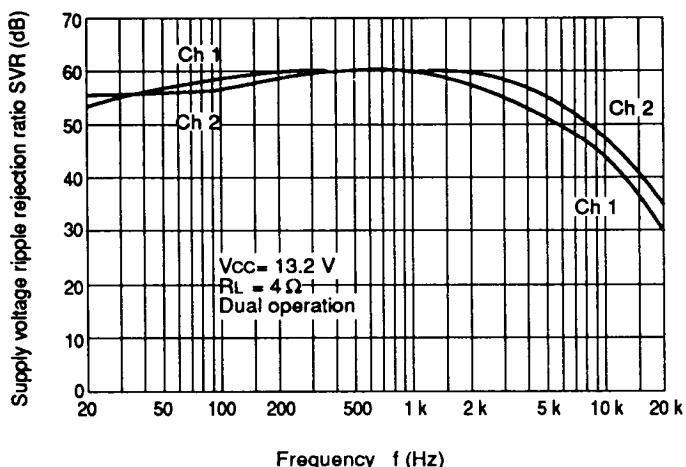
**Figure 2 HA13130 Characteristic Curves**



**Figure 2 HA13130 Characteristic Curves (cont)**

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Supply voltage rejection ratio vs. frequency



Cross-talk vs. frequency

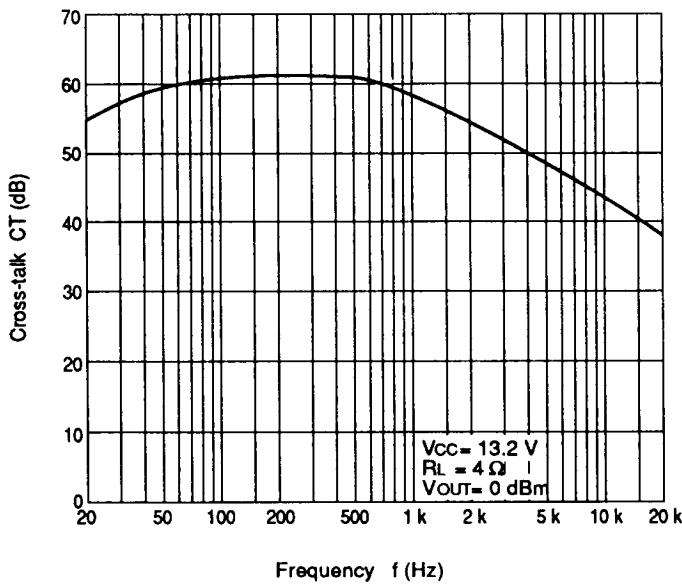
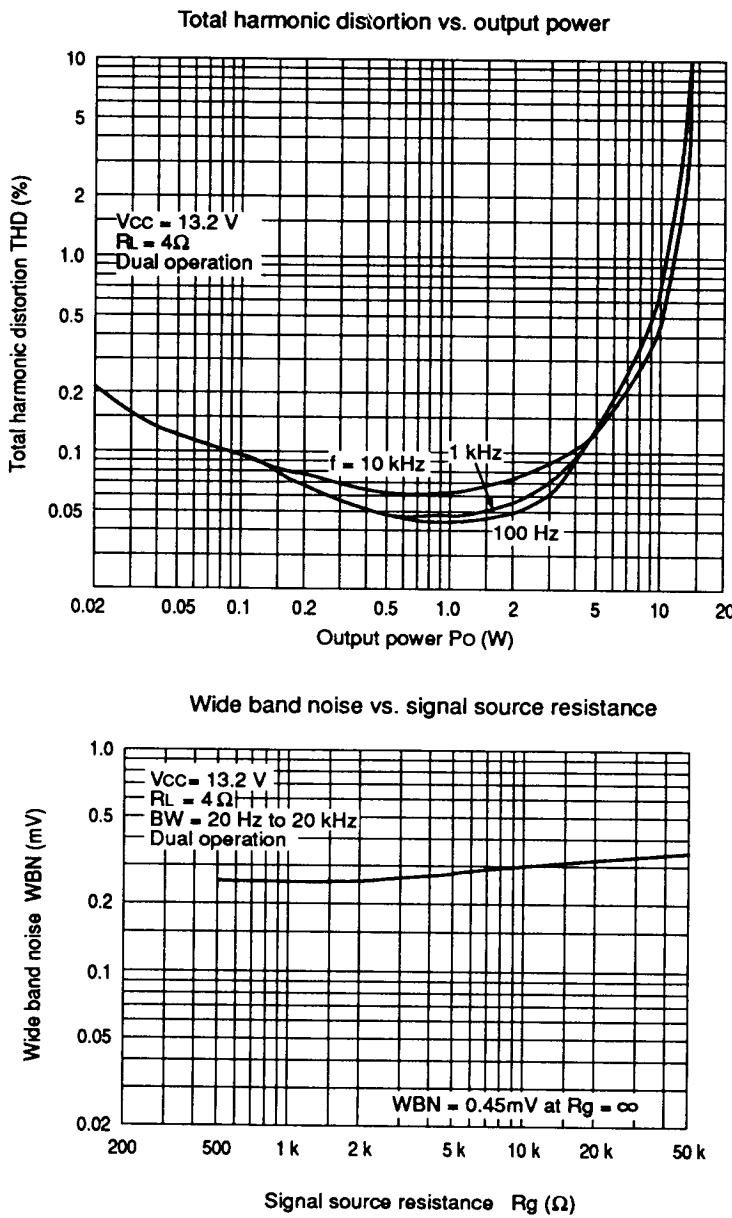


Figure 2 HA13130 Characteristic Curves (cont)

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**Figure 2 HA13130 Characteristic Curves (cont)**

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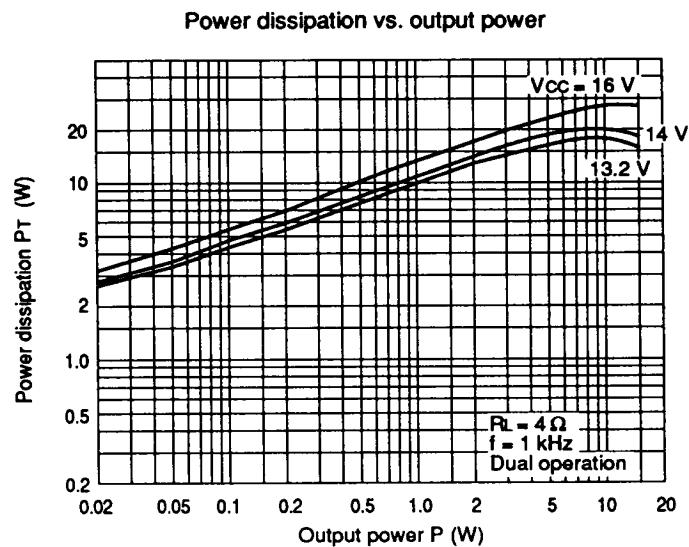
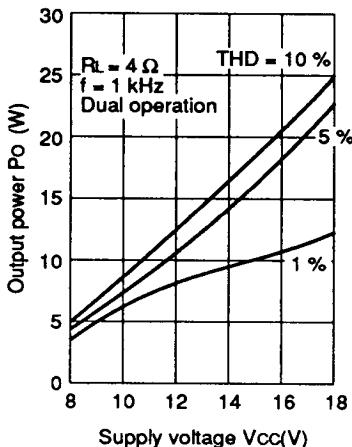


Figure 2 HA13130 Characteristic Curves (cont)



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Power dissipation vs. output power (1)



Power dissipation vs. output power (2)

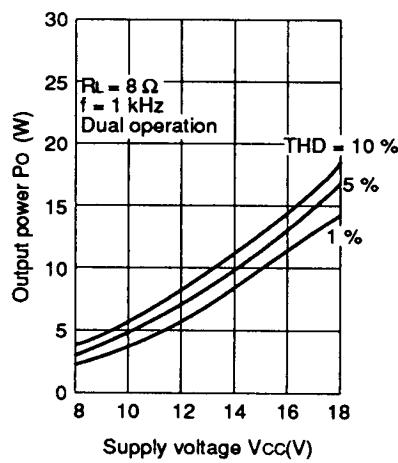
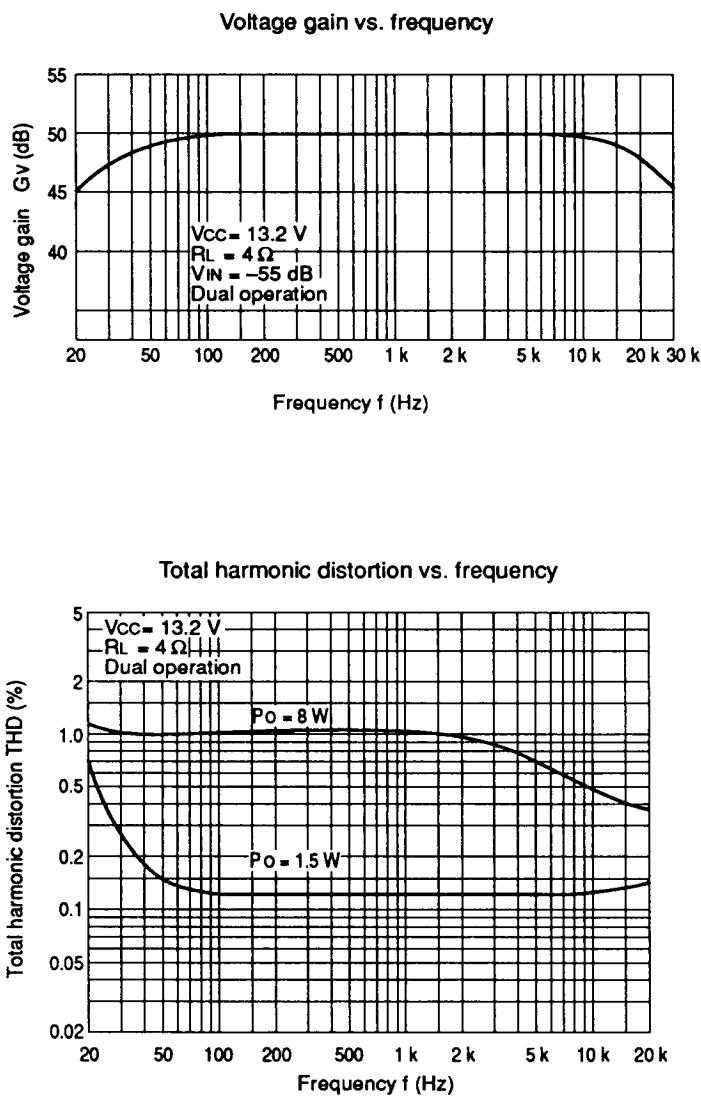


Figure 3 HA13127 Characteristic Curves

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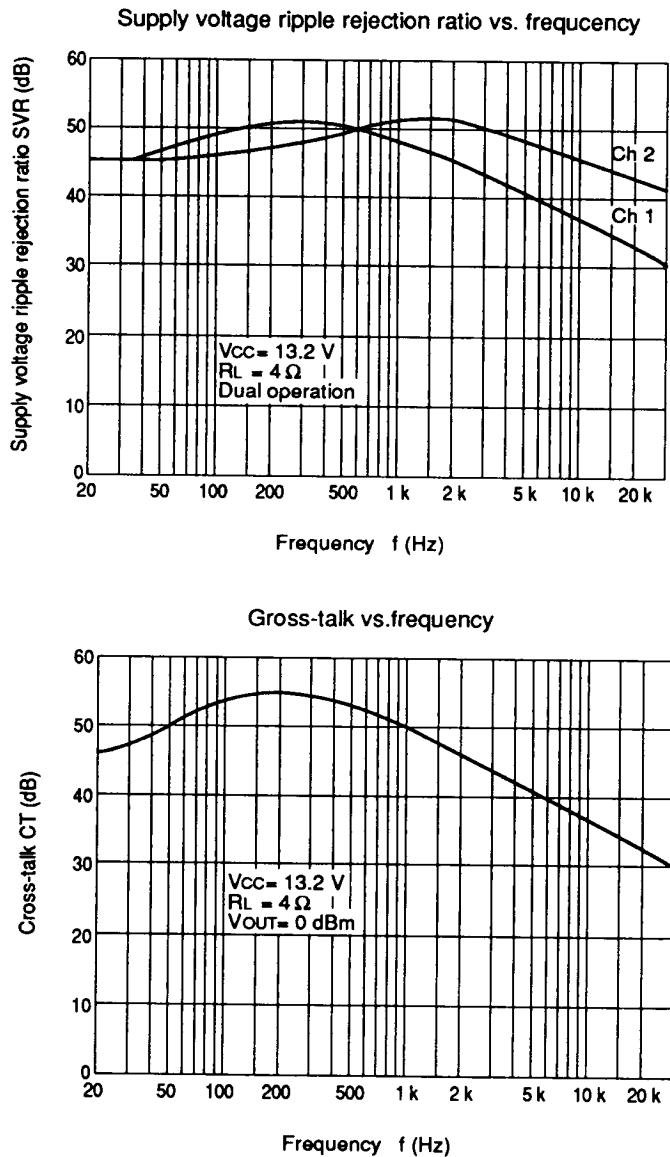
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**Figure 3 HA13127 Characteristic Curves (cont)**



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**Figure 3 HA13127 Characteristic Curves (cont)**

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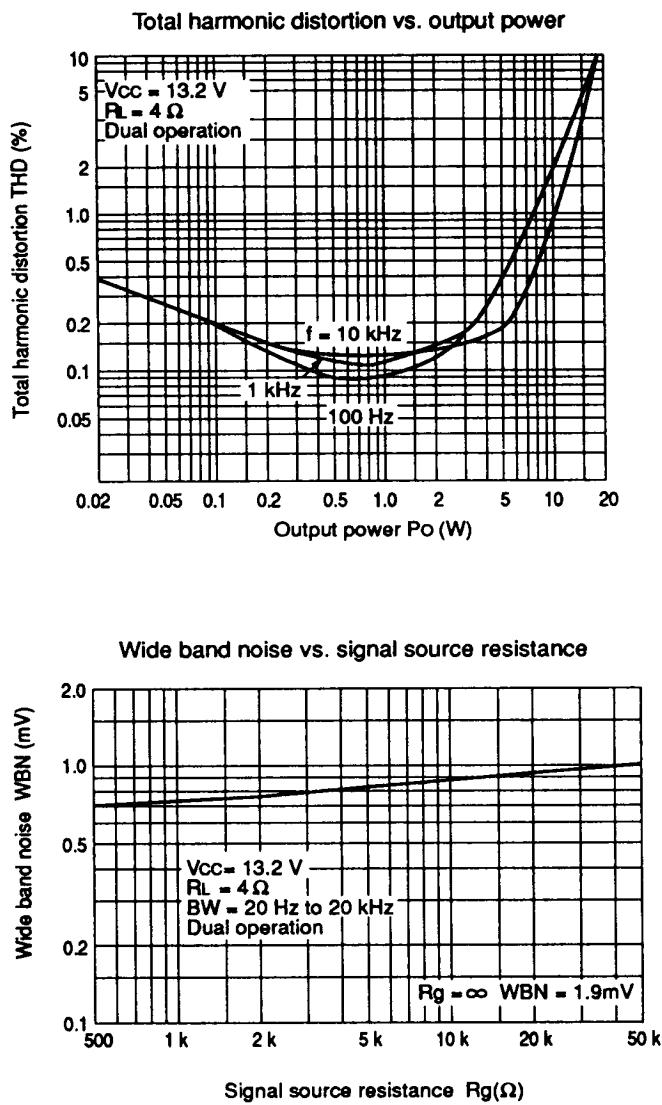


Figure 3 HA13127 Characteristic Curves (cont)



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