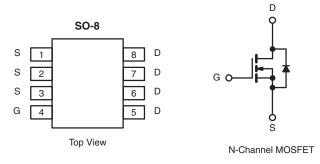
## N-Channel 30-V (D-S) MOSFET

| PRODUCT SUMMARY     |                                  |                                 |                       |  |  |  |
|---------------------|----------------------------------|---------------------------------|-----------------------|--|--|--|
| V <sub>DS</sub> (V) | R <sub>DS(on)</sub> (Ω)          | I <sub>D</sub> (A) <sup>a</sup> | Q <sub>g</sub> (Typ.) |  |  |  |
| 30                  | 0.0095 at V <sub>GS</sub> = 10 V | 17                              | 8 nC                  |  |  |  |
|                     | 0.013 at $V_{GS}$ = 4.5 V        | 14.5                            | 0110                  |  |  |  |



#### **FEATURES**

- Halogen-free According to IEC 61249-2-21
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> and UIS Tested

#### **APPLICATIONS**

- Notebook CPU Core
  - High-Side Switch



| Parameter   |                        | Symbol                            | Limit               | Unit |  |
|---|------------------------|-----------------------------------|---------------------|------|--|
| Drain-Source Voltage                                |                        | V <sub>DS</sub>                   | 30                  | V    |  |
| Gate-Source Voltage                                 |                        | V <sub>GS</sub>                   | ± 20                |      |  |
|   | T <sub>C</sub> = 25 °C |                                   | 17                  |      |  |
| Continuous Drain Current (T <sub>.1</sub> = 150 °C) | T <sub>C</sub> = 70 °C |                                   | 13.5                |      |  |
| Continuous Drain Current (1j = 150°C)               | T <sub>A</sub> = 25 °C |                                   | 12 <sup>b, c</sup>  |      |  |
|   | T <sub>A</sub> = 70 °C |                                   | 9.6 <sup>b, c</sup> | A    |  |
| Pulsed Drain Current                                |                        | I <sub>DM</sub>                   | 50                  | A    |  |
| Quality of the Design Divide Quart                  | T <sub>C</sub> = 25 °C |                                   | 4.5                 |      |  |
| Continuous Source-Drain Diode Current               | T <sub>A</sub> = 25 °C | Is Is                             | 2.2 <sup>b, c</sup> |      |  |
| Single Pulse Avalanche Current                      | L = 0.1 mH             | I <sub>AS</sub>                   | 20                  |      |  |
| Avalanche Energy                                    |                        | E <sub>AS</sub>                   | 20                  | mJ   |  |
|   | T <sub>C</sub> = 25 °C |                                   | 5                   |      |  |
| Movimum Dower Discinction                           | T <sub>C</sub> = 70 °C | P <sub>D</sub>                    | 3.2                 | w    |  |
| Maximum Power Dissipation                           | T <sub>A</sub> = 25 °C | ГD                                | 2.5 <sup>b, c</sup> | vv   |  |
|   | T <sub>A</sub> = 70 °C | 1                                 | 1.6 <sup>b, c</sup> |      |  |
| Operating Junction and Storage Temperature Range    |                        | T <sub>J</sub> , T <sub>stg</sub> | - 55 to 150         | °C   |  |

| THERMAL RESISTANCE RATINGS                  |              |                   |         |         |      |  |  |
|---|--------------|-------------------|---------|---------|------|--|--|
| Parameter                                   |              | Symbol            | Typical | Maximum | Unit |  |  |
| Maximum Junction-to-Ambient <sup>b, d</sup> | t ≤ 10 s     | R <sub>thJA</sub> | 38      | 50      | °C/W |  |  |
| Maximum Junction-to-Foot (Drain)            | Steady State | R <sub>thJF</sub> | 20      | 25      | 0/11 |  |  |

Notes:

a. Based on  $T_C = 25 \text{ °C}$ . b. Surface Mounted on 1" x 1" FR4 board.

c. t = 10 s.

d. Maximum under Steady State conditions is 85  $^{\circ}\text{C/W}.$ 

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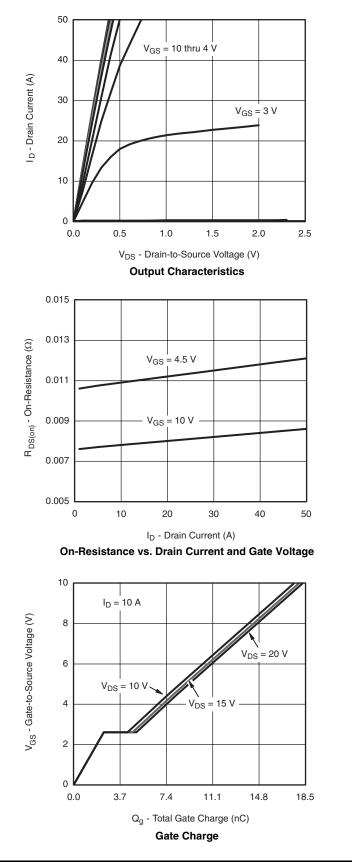
| Parameter                                     | Symbol                  | Test Conditions   | Min. | Тур.   | Max.   | Unit  |  |
|---|-------------------------|---|------|--------|--------|-------|--|
| Static  |                         |   |      |        |        |       |  |
| Drain-Source Breakdown Voltage                | V <sub>DS</sub>         | $V_{GS} = 0 V, I_D = 250 \mu A$   | 30   |        |        | V     |  |
| V <sub>DS</sub> Temperature Coefficient       | $\Delta V_{DS}/T_{J}$   | L 050   |      | 34     |        | mV/°C |  |
| V <sub>GS(th)</sub> Temperature Coefficient   | $\Delta V_{GS(th)}/T_J$ | - I <sub>D</sub> = 250 μA   |      | - 4.7  |        |       |  |
| Gate-Source Threshold Voltage                 | V <sub>GS(th)</sub>     | $V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$   | 1.0  |        | 2.2    | V     |  |
| Gate-Source Leakage                           | I <sub>GSS</sub>        | $V_{DS} = 0 V, V_{GS} = \pm 20 V$   |      |        | ± 100  | nA    |  |
| 7   | I <sub>DSS</sub>        | V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V   |      |        | 1      |       |  |
| Zero Gate Voltage Drain Current               |                         | $V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 ^{\circ}\text{C}$    |      |        | 10     | μA    |  |
| On-State Drain Current <sup>a</sup>           | I <sub>D(on)</sub>      | $V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$                                       | 30   |        |        | Α     |  |
|   | R                       | V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A   |      | 0.0078 | 0.0095 | Ω     |  |
| Drain-Source On-State Resistance <sup>a</sup> | R <sub>DS(on)</sub>     | V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 7 A   |      | 0.0108 | 0.0130 |       |  |
| Forward Transconductance <sup>a</sup>         | 9 <sub>fs</sub>         | V <sub>DS</sub> = 15 V, I <sub>D</sub> = 10 A   |      | 30     |        | S     |  |
| Dynamic <sup>b</sup>                          | I                       |   | I    |        |        |       |  |
| Input Capacitance                             | C <sub>iss</sub>        |   |      | 985    |        |       |  |
| Output Capacitance                            | C <sub>oss</sub>        | V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz                                      |      | 205    |        | pF    |  |
| Reverse Transfer Capacitance                  | C <sub>rss</sub>        | 1   |      | 76     |        |       |  |
| Total Gate Charge                             | $Q_g = V_{DS} = 15$     | $V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 10 \text{ A}$                          |      | 18     | 27     |       |  |
|   |                         |   |      | 8      | 12     |       |  |
| Gate-Source Charge                            | Q <sub>gs</sub>         | $V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 10 \text{ A}$                         |      | 2.4    |        |       |  |
| Gate-Drain Charge                             | Q <sub>gd</sub>         |   |      | 2.3    |        |       |  |
| Gate Resistance                               | Rg                      | f = 1 MHz   | 0.3  | 1.3    | 2.6    | Ω     |  |
| Turn-On Delay Time                            | t <sub>d(on)</sub>      |   |      | 14     | 25     |       |  |
| Rise Time                                     | t <sub>r</sub>          | $V_{DD}$ = 15 V, $R_L$ = 1.5 $\Omega$   |      | 12     | 24     |       |  |
| Turn-Off Delay Time                           | t <sub>d(off)</sub>     | $I_{D}\cong$ 10 A, $V_{GEN}$ = 4.5 V, $R_{g}$ = 1 $\Omega$                                    |      | 19     | 35     |       |  |
| Fall Time                                     | t <sub>f</sub>          |   |      | 9      | 18     |       |  |
| Turn-On Delay Time                            | t <sub>d(on)</sub>      |   |      | 8      | 16     | ns    |  |
| Rise Time                                     | t <sub>r</sub>          | $V_{DD}$ = 15 V, $R_L$ = 1.5 $\Omega$   |      | 10     | 20     |       |  |
| Turn-Off Delay Time                           | t <sub>d(off)</sub>     | $\text{I}_\text{D}\cong$ 10 A, $\text{V}_\text{GEN}$ = 10 V, $\text{R}_\text{g}$ = 1 $\Omega$ |      | 16     | 30     |       |  |
| Fall Time                                     | t <sub>f</sub>          |   |      | 9      | 18     |       |  |
| Drain-Source Body Diode Characteristi         | cs                      |   |      |        |        |       |  |
| Continuous Source-Drain Diode Current         | ۱ <sub>S</sub>          | T <sub>C</sub> = 25 °C  |      |        | 4.5    | ۸     |  |
| Pulse Diode Forward Current <sup>a</sup>      | I <sub>SM</sub>         |   |      |        | 50     | A     |  |
| Body Diode Voltage                            | V <sub>SD</sub>         | I <sub>S</sub> = 3 A  |      | 0.76   | 1.1    | V     |  |
| Body Diode Reverse Recovery Time              | t <sub>rr</sub>         |   |      | 14     | 28     | ns    |  |
| Body Diode Reverse Recovery Charge            | Q <sub>rr</sub>         | $\frac{1}{1}$   |      | 5      | 10     | nC    |  |
| Reverse Recovery Fall Time                    | ta                      | I <sub>F</sub> = 10 A, dl/dt = 100 A/μs, T <sub>J</sub> = 25 °C                               |      | 8      |        |       |  |
| Reverse Recovery Rise Time                    | t <sub>b</sub>          | -   |      | 6      |        | ns    |  |

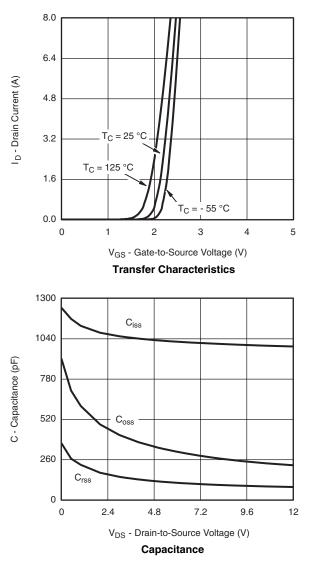
Notes:

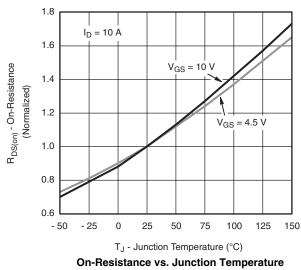
a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %

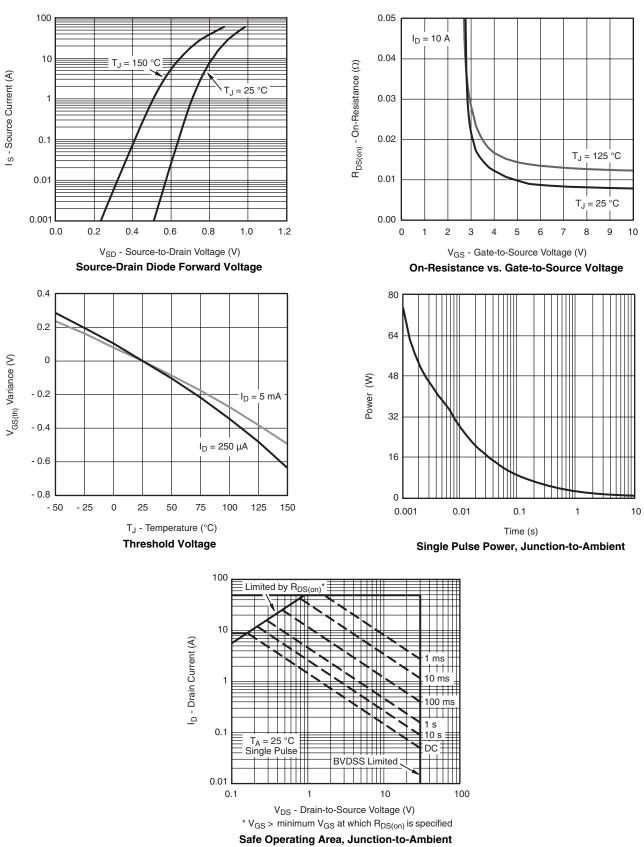
b. Guaranteed by design, not subject to production testing.

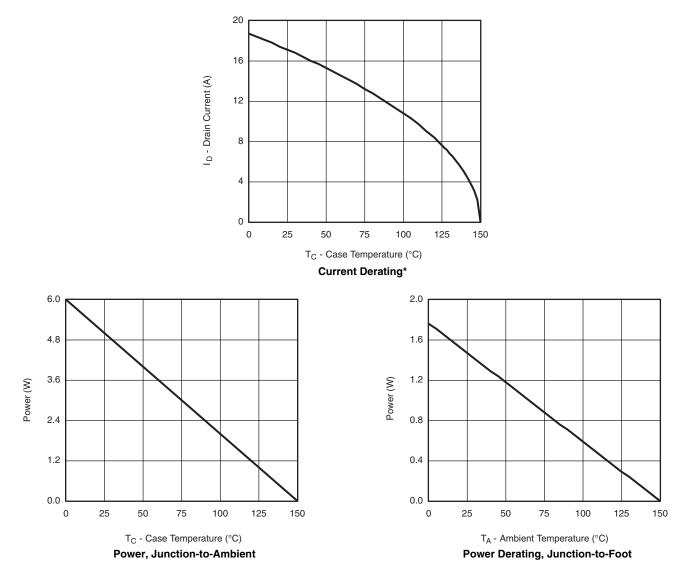
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



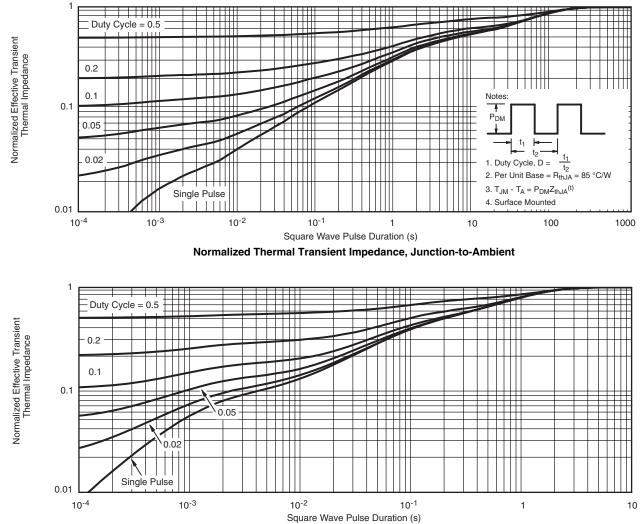








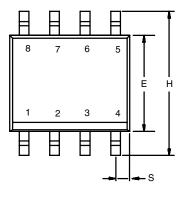
\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

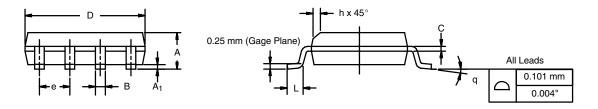


Normalized Thermal Transient Impedance, Junction-to-Foot



# SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012

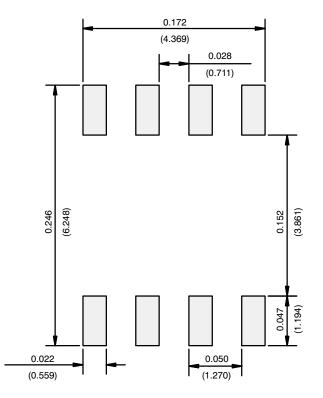




|   | MILLIM | IETERS | INCHES    |       |  |
|---|--------|--------|-----------|-------|--|
| DIM   | Min    | Мах    | Min       | Max   |  |
| A   | 1.35   | 1.75   | 0.053     | 0.069 |  |
| A <sub>1</sub>                              | 0.10   | 0.20   | 0.004     | 0.008 |  |
| В   | 0.35   | 0.51   | 0.014     | 0.020 |  |
| С   | 0.19   | 0.25   | 0.0075    | 0.010 |  |
| D   | 4.80   | 5.00   | 0.189     | 0.196 |  |
| E   | 3.80   | 4.00   | 0.150     | 0.157 |  |
| е   | 1.27   | BSC    | 0.050 BSC |       |  |
| н   | 5.80   | 6.20   | 0.228     | 0.244 |  |
| h   | 0.25   | 0.50   | 0.010     | 0.020 |  |
| L   | 0.50   | 0.93   | 0.020     | 0.037 |  |
| q   | 0°     | 8°     | 0°        | 8°    |  |
| S   | 0.44   | 0.64   | 0.018     | 0.026 |  |
| ECN: C-06527-Rev. I, 11-Sep-06<br>DWG: 5498 |        |        |           |       |  |



**RECOMMENDED MINIMUM PADS FOR SO-8** 



Recommended Minimum Pads Dimensions in Inches/(mm)

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