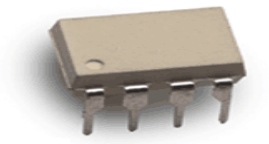
HCPL-7840
Isolation Amplifier

Description

The HCPL-7840 isolation amplifier family was designed for current sensing in electronic motor drives. In a typical implementation, motor currents flow through an external resistor and the resulting analog voltage drop is sensed by the HCPL-7840. A differential output voltage is created on the other side of the HCPL-7840 optical isolation barrier. This differential output voltage is proportional to the motor current and can be converted to a single-ended signal by using an op-amp. Since common-mode voltage swings of several hundred volts in tens of nanoseconds are common in modern switching inverter motor drives, the HCPL-7840 was designed to ignore very high common-mode transient slew rates (of at least 10 kV/ms).

Lifecycle status: Active



The high CMR capability of the HCPL-7840 isolation amplifier provides the precision and stability needed to accurately monitor motor current in high noise motor control environments, providing for smoother control (less "torque ripple") in various types of motor control applications.

The product can also be used for general analog signal isolation applications requiring high accuracy, stability, and linearity under similarly severe noise conditions. For general applications, we recommend the HCPL-7840 (gain tolerance of /- 5%). The HCPL-7840 utilizes sigma delta (S-D) analog-to-digital converter technology, chopper stabilized amplifiers, and a fully differential circuit topology fabricated using Avago Technologies's 0.8 mm CMOS IC process.

Together, these features deliver unequaled isolation-mode noise rejection, as well as excellent offset and gain accuracy and stability over time and temperature. This performance is delivered in a compact, auto-insertable, industry standard 8-pin DIP package that meets worldwide regulatory safety standards. (A gull-wing surface mount option #300 is also available).

Features

- 15 kV/ms Common-Mode Rejection at VCM = 1000 V
- Compact, Auto-Insertable Standard 8-pin DIP Package
- 0.00025 V/V/ degrees C Gain Drift vs. Temperature
- 0.3 mV Input Offset Voltage
- 100 kHz Bandwidth
- 0.004% Nonlinearity
- Worldwide Safety Approval: UL 1577 (3750 Vrms/1 min.) and CSA (pending), IEC/EN/DIN EN 60747-5-2 (option 060 only)

Advanced Sigma-Delta (S-D) A/D Converter Technology

Fully Differential Circuit Topology

0.8 mm CMOS IC Technology

Options available are:

No Option = Standard DIP package, 50 per tube
060 = IEC/EN/DIN EN 60747-5-2 Option
300 = Surface Mount Option
500 = Tape/Reel Packaging Option, 1 k min. per reel
XXXE = Lead Free Option

Applications

Motor Phase and Rail Current Sensing

Inverter Current Sensing

Switched Mode Power Supply Signal Isolation

General Purpose Current Sensing and Monitoring

General Purpose Analog Signal Isolation

HCPL-7840
Isolation Amplifier

AVAGO TECHNOLOGIES

Data Sheet

Lead (Pb) Free
RoHS 6 fully compliant

RoHS 6 fully compliant options available;
-xxxE denotes a lead-free product

Description

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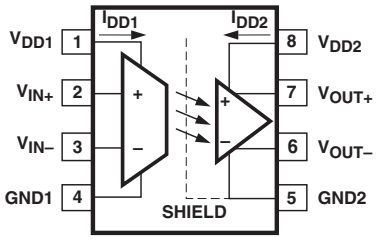
- Compact, auto-insertable standard 8-pin DIP package
- 0.00025 V/V/°C gain drift vs. temperature
- 0.3 mV input offset voltage
- 100 kHz bandwidth
- 0.004% nonlinearity
- Worldwide safety approval:
  UL 1577 (3750 Vrms/1 min.) and CSA, IEC/EN/DIN EN 60747-5-2 (Option #060 only)
- Advanced Sigma-Delta (Σ-Δ) A/D converter technology
- Fully differential circuit topology
- 0.8 µm CMOS IC technology

Applications

- Motor phase and rail current sensing
- Inverter current sensing
- Switched mode power supply signal isolation
- General purpose current sensing and monitoring
- General purpose analog signal isolation

Functional Diagram



A 0.1 µF bypass capacitor must be connected between pins 1 and 4 and between pins 5 and 8.

CAUTION: It is advised that normal static precautions be taken in handling and assembly of this component to prevent damage and/or degradation which may be induced by ESD.

Ordering Information

HCPL-7840 is UL Recognized with 3750 Vrms for 1 minute per UL1577.

Part Number	Option RoHS Compliant	Option non RoHS Compliant	Package	Surface Mount	Gull Wing	Tape & Reel	IEC/EN/DIN EN 60747-5-2	Quantity
HCPL-7840	-000E	no option	300 mil DIP-8					50 per tube
	-300E	#300		X	X			50 per tube
	-500E	#500		X	X	X		1000 per reel
	-060E	#060					X	50 per tube
	-360E	#360		X	X		X	50 per tube
	-560E	#560		X	X	X	X	1000 per reel

To order, choose a part number from the part number column and combine with the desired option from the option column to form an order entry.

Example 1:

HCPL-7840-560E to order product of Gull Wing Surface Mount package in Tape and Reel packaging with IEC/EN/DIN EN 60747-5-2 Safety Approval and RoHS compliant.

Example 2:

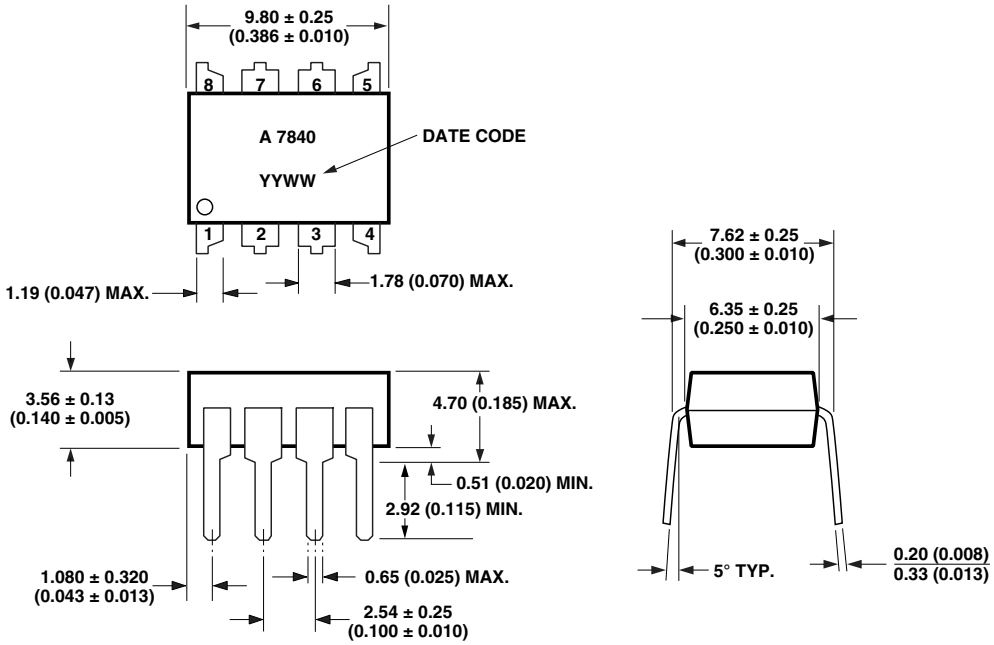
HCPL-7840 to order product of 300 mil DIP package in Tube packaging and non RoHS compliant.

Option datasheets are available. Contact your Avago sales representative or authorized distributor for information.

Remarks: The notation '#XXX' is used for existing products, while (new) products launched since July 15, 2001 and RoHS compliant will use '–XXXE.'

Package Outline Drawings

Standard DIP Package

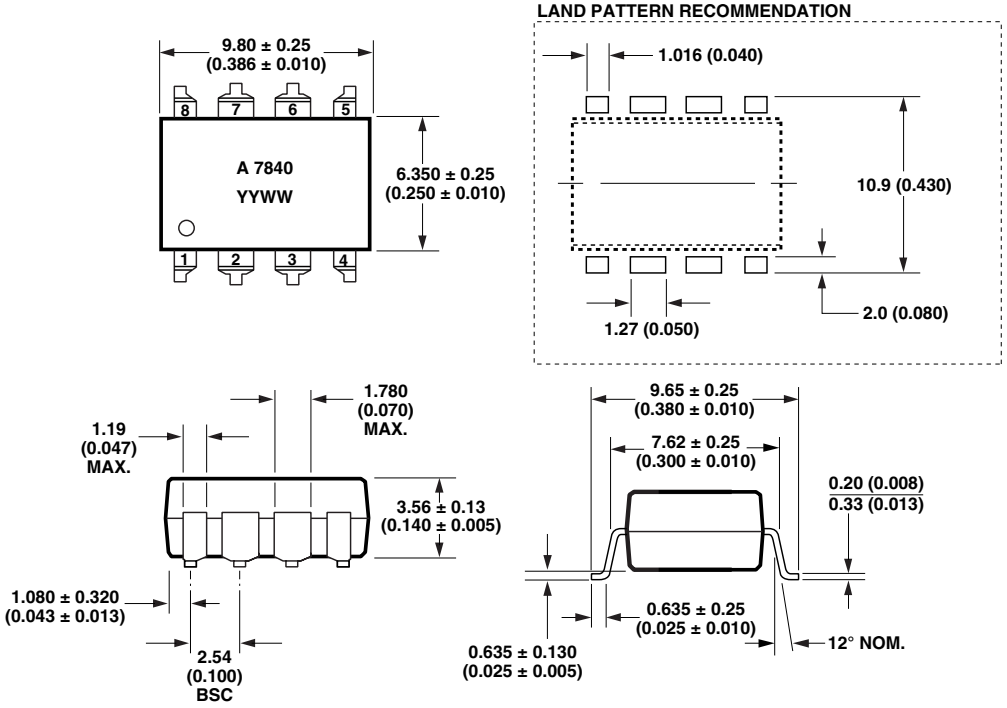


DIMENSIONS IN MILLIMETERS AND (INCHES).

NOTE: FLOATING LEAD PROTRUSION IS 0.5 mm (20 mils) MAX.

Note: Initial or continued variation in the color of the HCPL-7840's white mold compound is normal and does not affect device performance or reliability.

Gull Wing Surface Mount Option 300



DIMENSIONS IN MILLIMETERS (INCHES).
TOLERANCES (UNLESS OTHERWISE SPECIFIED): xx.xx = 0.01
xx.xxx = 0.005

LEAD COPLANARITY
MAXIMUM: 0.102 (0.004)

NOTE: FLOATING LEAD PROTRUSION IS 0.5 mm (20 mils) MAX.

Regulatory Information

The HCPL-7840 has been approved by the following organizations:

IEC/EN/DIN EN 60747-5-2
Approved under:
IEC 60747-5-2:1997 + A1:2002
EN 60747-5-2:2001 + A1:2002
DIN EN 60747-5-2 (VDE 0884
Teil 2):2003-01.

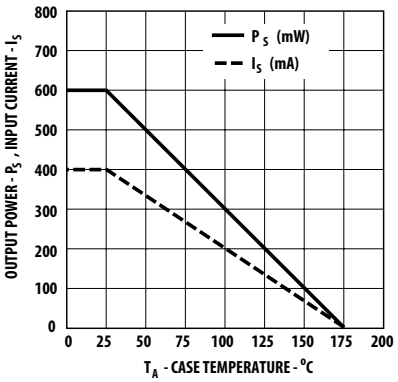
UL
Approval under UL 1577, component recognition program up to V_{ISO} = 3750 Vrms.

CSA
Approved under CSA Component Acceptance Notice #5, File CA 88324.

IEC/EN/DIN EN 60747-5-2 Insulation Characteristics*

Description	Symbol	Characteristic	Unit
Installation classification per DIN VDE 0110/1.89, Table 1			
for rated mains voltage ≤300 Vrms		I-IV	
for rated mains voltage ≤600 Vrms		I-III	
Climatic Classification		55/100/21	
Pollution Degree (DIN VDE 0110/1.89)		2	
Maximum Working Insulation Voltage	V_{IORM}	891	V_{PEAK}
Input to Output Test Voltage, Method b** V_{IORM} x 1.875 = $V_{PR,}$ 100% Production Test with t_m = 1 sec, Partial discharge < 5 pC	V_{PR}	1670	V_{PEAK}
Input to Output Test Voltage, Method a** V_{IORM} x 1.5 = $V_{PR,}$ Type and Sample Test, t_m = 60 sec, Partial discharge < 5 pC	V_{PR}	1336	V_{PEAK}
Highest Allowable Overvoltage (Transient Overvoltage t_{ini} = 10 sec)	V_{IOTM}	6000	V_{PEAK}
Safety-limiting values—maximum values allowed in the event of a failure.			
Case Temperature	T_S	175	°C
Input Current***	$I_{S,INPUT}$	400	mA
Output Power***	$P_{S,OUTPUT}$	600	mW
Insulation Resistance at T_S, V_{IO} = 500 V	R_S	$>10^9$	Ω

*Insulation characteristics are guaranteed only within the safety maximum ratings which must be ensured by protective circuits within the application. Surface Mount Classification is Class A in accordance with CECC00802.
**Refer to the optocoupler section of the Isolation and Control Components Designer's Catalog, under Product Safety Regulations section, IEC/EN/DIN EN 60747-5-2, for a detailed description of Method a and Method b partial discharge test profiles.
***Refer to the following figure for dependence of Ps and Is on ambient temperature.



Insulation and Safety Related Specifications

Parameter	Symbol	Value	Unit	Conditions
Minimum External Air Gap (Clearance)	L(101)	7.4	mm	Measured from input terminals to output terminals, shortest distance through air.
Minimum External Tracking (Creepage)	L(102)	8.0	mm	Measured from input terminals to output terminals, shortest distance path along body.
Minimum Internal Plastic Gap (Internal Clearance)		0.5	mm	Through insulation distance conductor to conductor, usually the straight line distance thickness between the emitter and detector.
Tracking Resistance (Comparative Tracking Index)	CTI	>175	Volts	DIN IEC 112/VDE 0303 Part 1
Isolation Group		III a		Material Group (DIN VDE 0110, 1/89, Table 1)

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit	Note
Storage Temperature	T_S	-55	125	°C	
Operating Temperature	T_A	-40	100		
Supply Voltage	V_{DD1}, V_{DD2}	0	5.5	V	
Steady-State Input Voltage	V_{IN+}, V_{IN-}	-2.0	V_{DD1} +0.5		
2 Second Transient Input Voltage		-6.0	V_{DD1} +0.5		
Output Voltage	V_{OUT}	-0.5	V_{DD2} +0.5		
Solder Reflow Temperature Profile	See **Solder Reflow Temperature Profile** Section				

Recommended Operating Conditions

Parameter	Symbol	Min.	Max.	Unit	Note
Ambient Operating Temperature	T_A	-40	85	°C	
Supply Voltage	V_{DD1}, V_{DD2}	4.5	5.5	V	
Input Voltage (accurate and linear)	V_{IN+}, V_{IN-}	-200	200	mV	1
Input Voltage (functional)	V_{IN+}, V_{IN-}	-2	2	V	

DC Electrical Specifications

Unless otherwise noted, all typicals and figures are at the nominal operating conditions of $V_{IN+} = 0$, $V_{IN-} = 0$ V, $V_{DD1} = V_{DD2} = 5$ V and $T_A = 25°C$; all Min./Max. specifications are within the Recommended Operating Conditions.

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions	Fig.	Note
Input Offset Voltage	V_{OS}	-2.0	0.3	2.0	mV	$T_A = 25°C$	1,2	
		-3.0		3.0	mV	$T_A = -40°C$ to $+85°C$	1,2	
Magnitude of Input Offset Change vs. Temperature	$\lvert\Delta V_{OS}/\Delta T_A\rvert$		3.0	10.0	µV/°C		3	2
Gain (± 5% Tol.)	G	7.60	8.00	8.40	V/V	-200 mV < V_{IN+} < 200 mV, $T_A = 25°C$	4,5,6	3
Magnitude of V_{OUT} Gain Change vs. Temperature	$\lvert\Delta G/\Delta T_A\rvert$		0.00025		V/V/°C			4
V_{OUT} 200 mV Nonlinearity	NL_{200}		0.0037	0.35	%	-200 mV < V_{IN+} < 200 mV	7,8	5
Magnitude of V_{OUT} 200 mV Nonlinearity Change vs. Temperature	$\lvert dNL_{200}/dT\rvert$		0.0002		% / °C			
V_{OUT} 100 mV Nonlinearity	NL_{100}		0.0027	0.2	%	-100 mV < V_{IN+} < 100 mV		6
Maximum Input Voltage before V_{OUT} Clipping	$\lvert V_{IN+}\rvert_{MAX}$		308.0		mV		9	
Input Supply Current	I_{DD1}		10.86	15.5	mA	V_{IN+} = 400 mV	10	7
Output Supply Current	I_{DD2}		11.56	15.5		V_{IN+} = -400 mV		8
Input Current	I_{IN+}		-0.5	5.0	µA		11	9
Magnitude of Input Bias Current vs. Temperature Coefficient	$\lvert dI_{IN}/dT\rvert$		+0.45		nA/°C		11	
Output Low Voltage	V_{OL}		1.29		V			10
Output High Voltage	V_{OH}		3.80		V			10
Output Common-Mode Voltage	V_{OCM}	2.2	2.545	2.8	V			
Output Short-Circuit Current	$\lvert I_{OSC}\rvert$		18.6		mA			11
Equivalent Input Impedance	R_{IN}		500		kΩ			
V_{OUT} Output Resistance	R_{OUT}		15		Ω			
Input DC Common-Mode Rejection Ratio	$CMRR_{IN}$		76.1		dB			12

AC Electrical Specifications

Unless otherwise noted, all typicals and figures are at the nominal operating conditions of V_{IN+} = 0, V_{IN-} = 0 V, V_{DD1} = V_{DD2} = 5 V and T_A = 25°C; all Min./Max. specifications are within the Recommended Operating Conditions.

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions	Fig.	Note
V_{OUT} Bandwidth (-3 dB)	BW	50	100		kHz	V_{IN+} = 200 mV_{pk-pk} sine wave.	12,13	
V_{OUT} Noise	N_{OUT}		31.5		mVrms	V_{IN+} = 0.0 V		13
V_{IN} to V_{OUT} Signal Delay (50 – 10%)	t_{PD10}		2.03	3.3	µs	Measured at output of MC34081 on Figure 15. V_{IN+} = 0 mV to 150 mV step.	14,15	
V_{IN} to V_{OUT} Signal Delay (50 – 50%)	t_{PD50}		3.47	5.6				
V_{IN} to V_{OUT} Signal Delay (50 – 90%)	t_{PD90}		4.99	9.9				
V_{OUT} Rise/Fall Time (10 – 90%)	$t_{R/F}$		2.96	6.6				
Common Mode Transient Immunity	CMTI	10.0	15.0		kV/µs	V_{CM} = 1 kV, T_A = 25°C	16	14
Power Supply Rejection	PSR		170		mVrms	With recommended application circuit.		15

Package Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions	Fig.	Note
Input-Output Momentary Withstand Voltage	V_{ISO}	3750			Vrms	RH < 50%, t = 1 min., T_A = 25°C		16,17
Resistance (Input-Output)	R_{I-O}		$>10^9$		Ω	V_{I-O} = 500 V_{DC}		18
Capacitance (Input-Output)	C_{I-O}		1.2		pF	F = 1 MHz		18